## **ACKNOWLEDGEMENT RECEIPT**

## **Electronic Version**

Stylesheet Version v01

Title of Invention

## METHOD FOR REDUCING SHALLOW TRENCH ISOLATION CONSUMPTION IN SEMICONDUCTOR DEVICES

Submision Type: Utility Patent Filing

Application Number:

10/605727

EFS ID: 49601

Server Response:

Confirmation Code	Message
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First Named Applicant: Bruce Doris

Attorney Docket Number: FIS920030256US1

Timestamp: 2003-10-22 14:11:35 EDT

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